

FIG.1

10

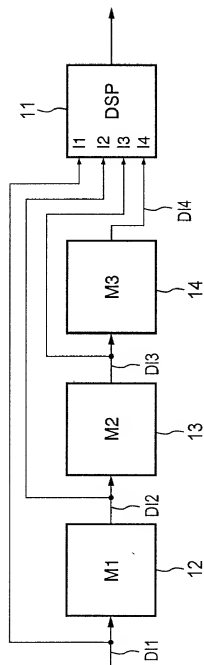


FIG.2

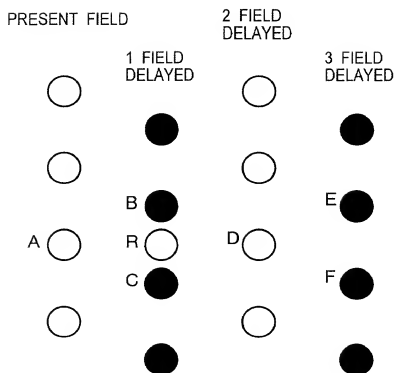


FIG. 3

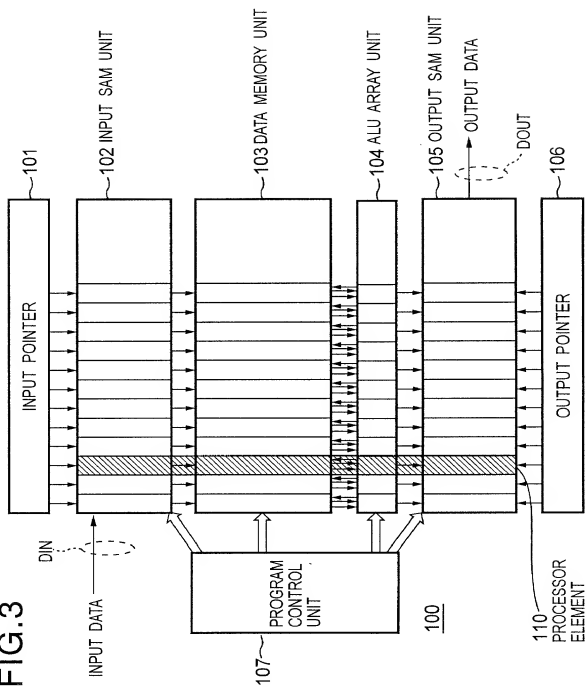


FIG.4A INPUT H SYNC

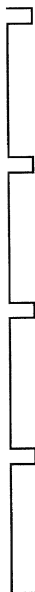


FIG.4B INPUT DATA



FIG.4C

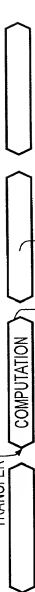


FIG.4D OUTPUT DATA



FIG.4E OUTPUT H SYNC

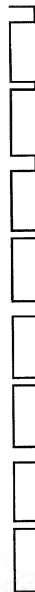


FIG.5

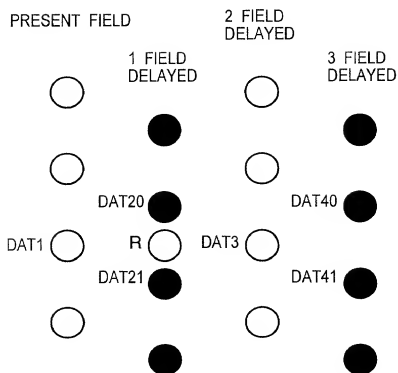


FIG.6

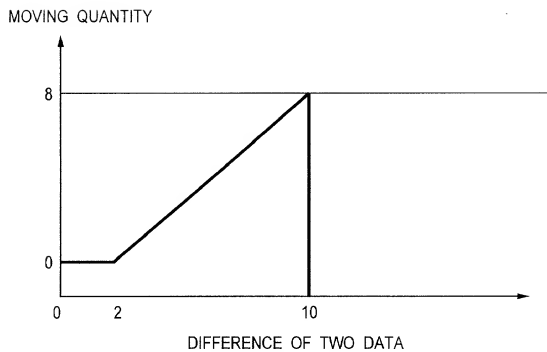


FIG.7

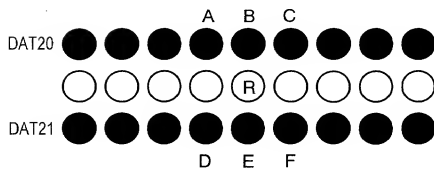


FIG.8

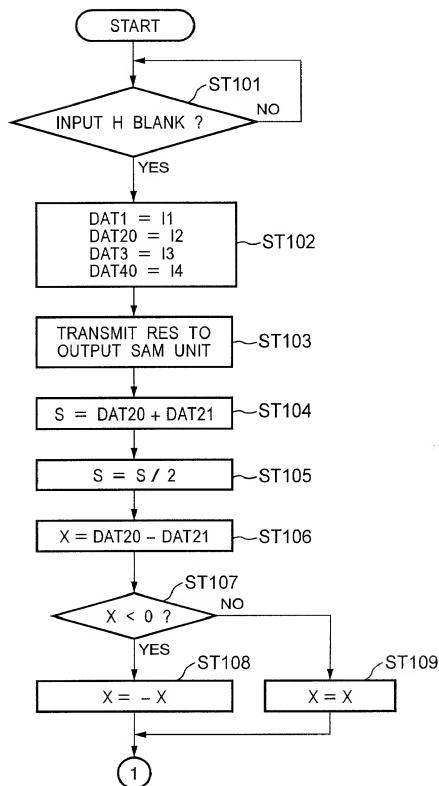


FIG.9

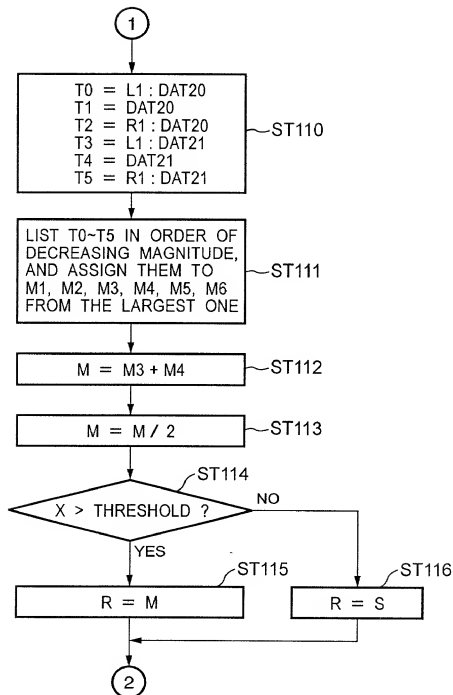


FIG.10

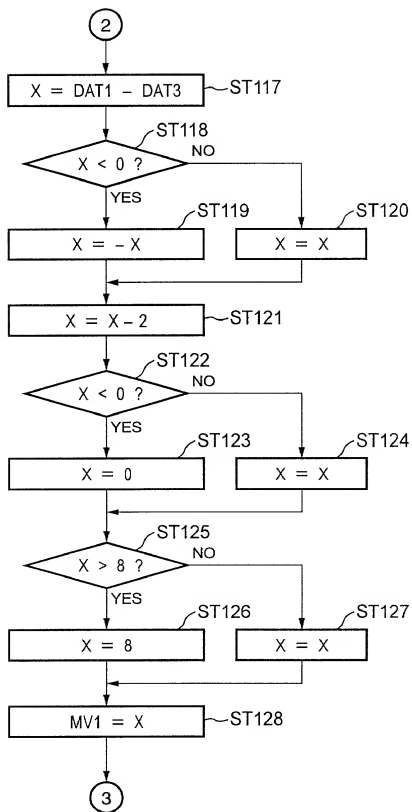


FIG.11

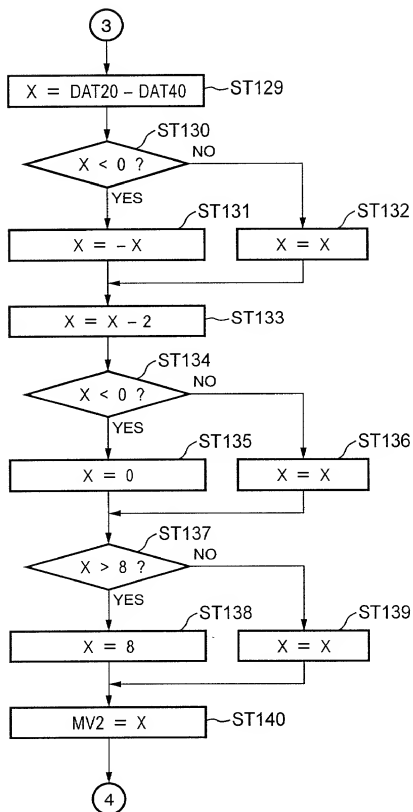


FIG.12

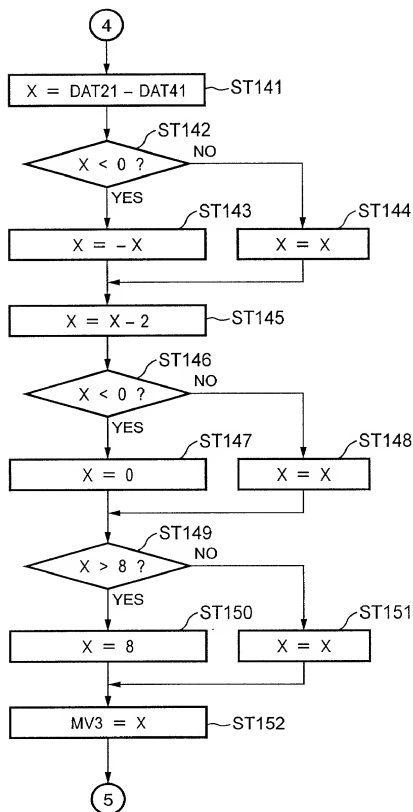


FIG.13

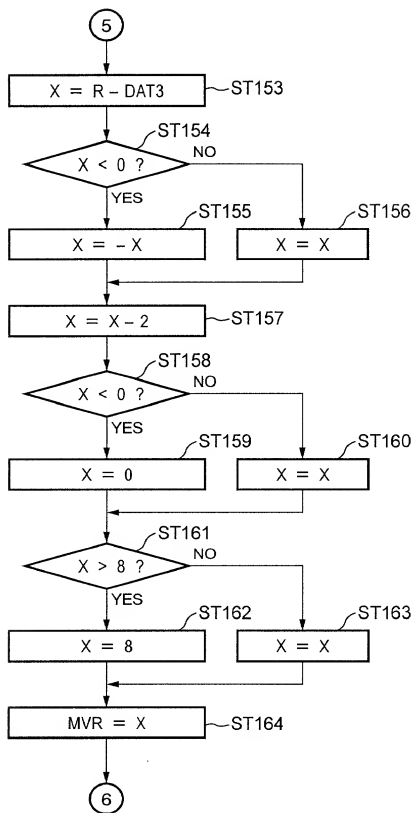


FIG.14

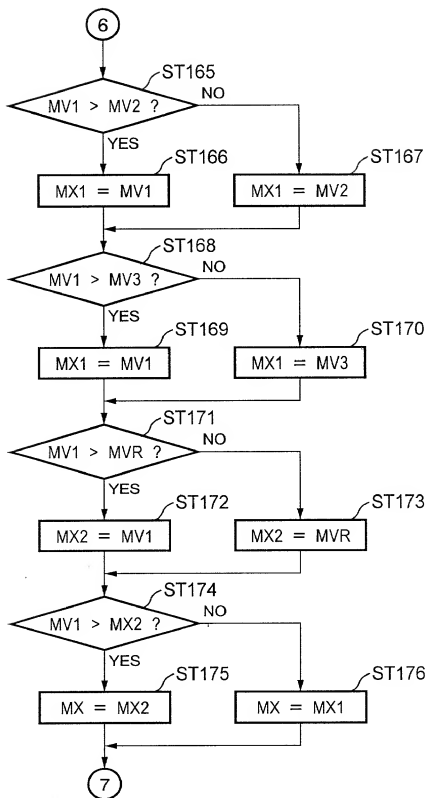


FIG.15

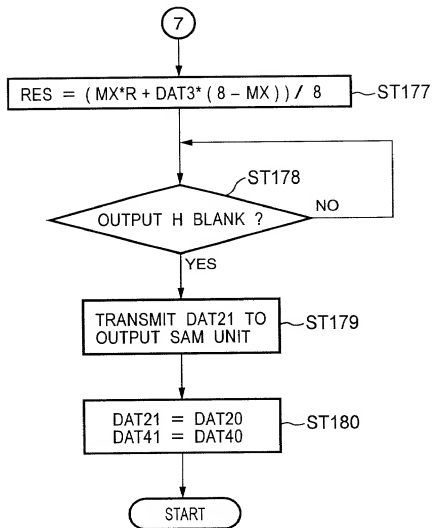


FIG.16

20

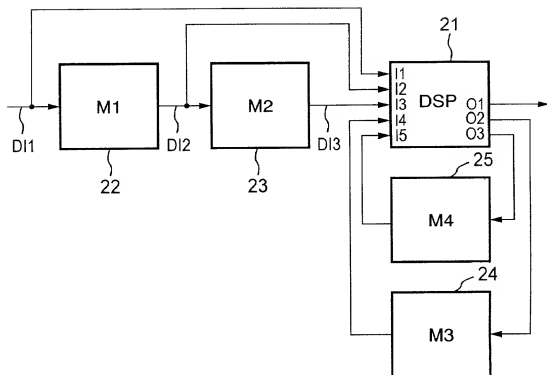


FIG.17

PRESENT FIELD

2 FIELD
DELAYED

1 FIELD
DELAYED

3 FIELD
DELAYED



A

B

R

C

D

E

F

FIG.18A INPUT H SYNC



FIG.18B INPUT DATA



FIG.18C

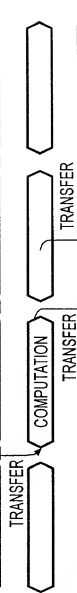


FIG.18D OUTPUT DATA



FIG.18E OUTPUT H SYNC

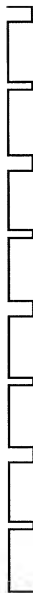


FIG.19

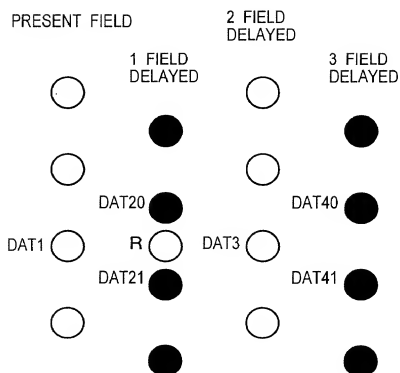


FIG.20

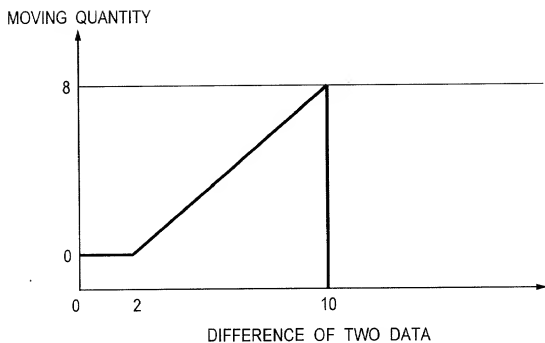


FIG.21

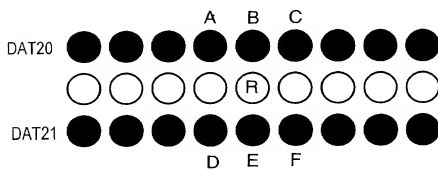


FIG.22

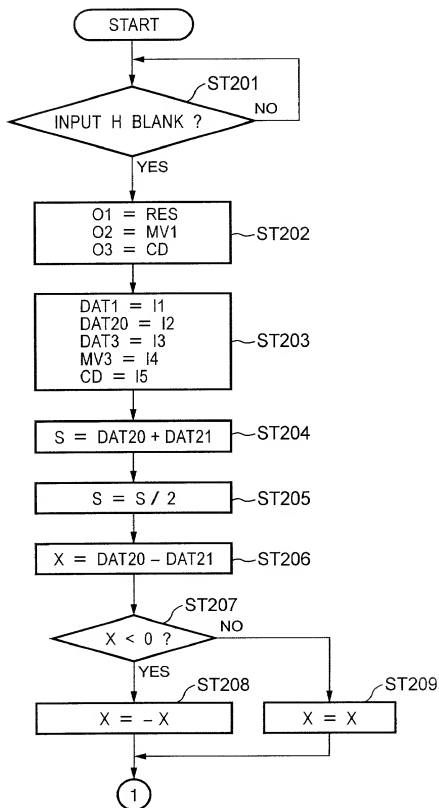


FIG.23

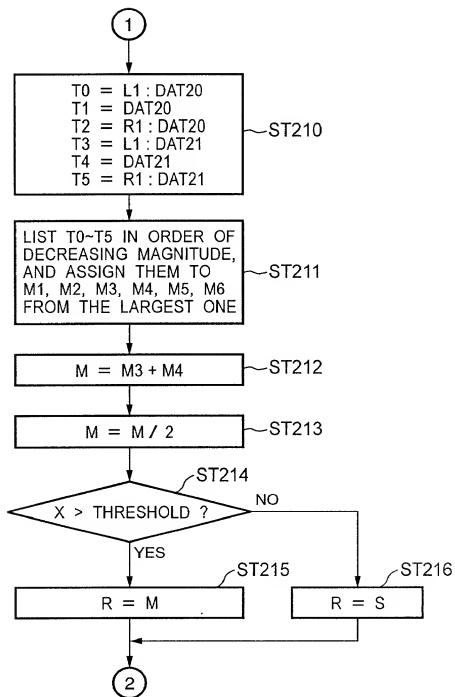


FIG.24

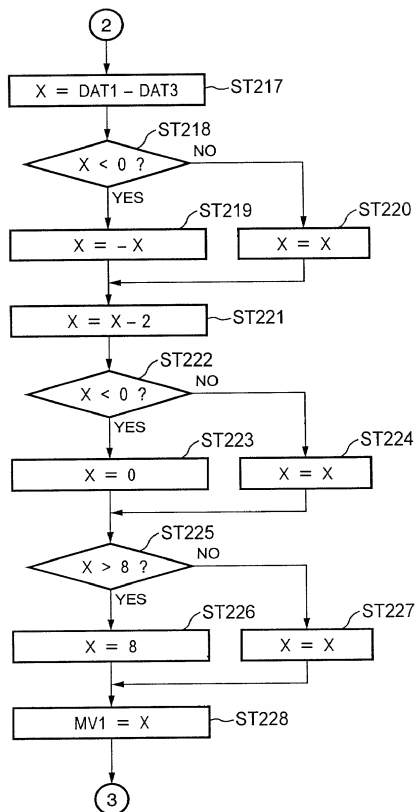


FIG. 25

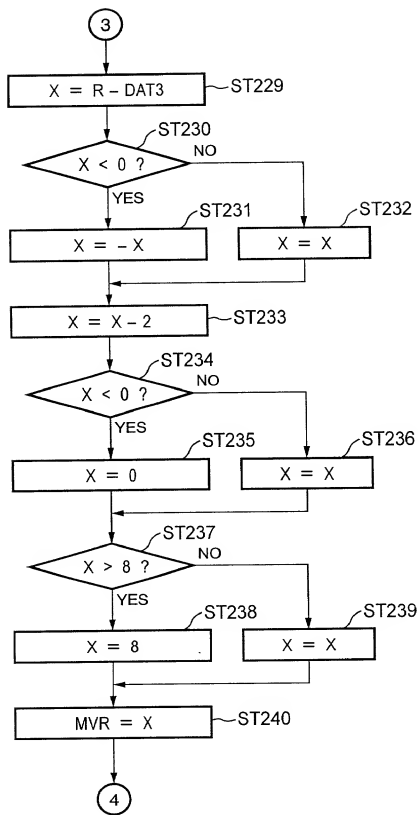


FIG.26

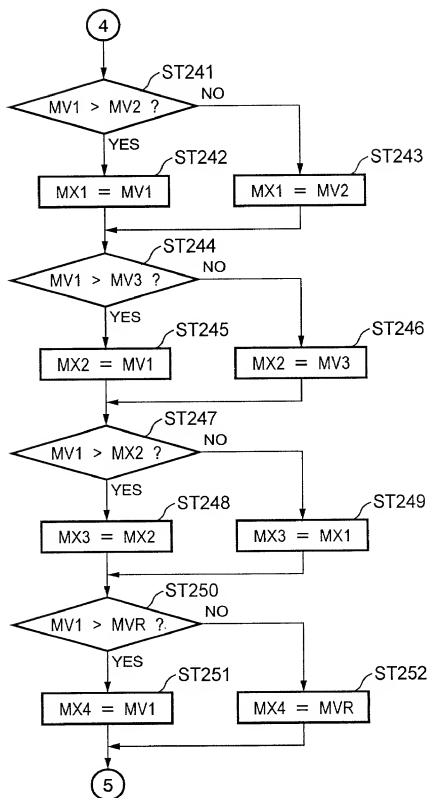


FIG.27

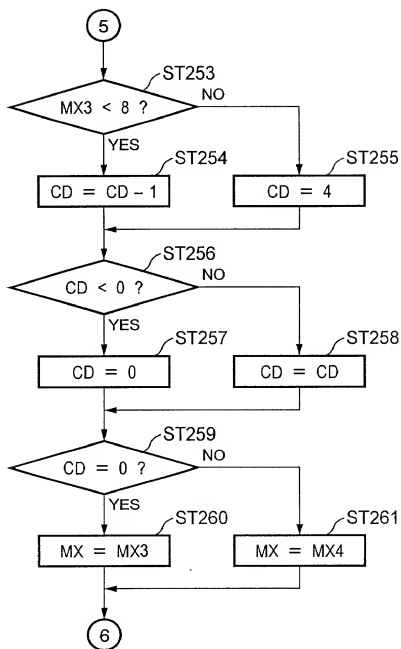


FIG.28

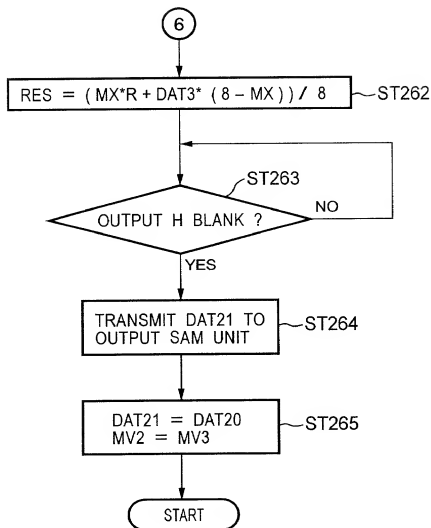


FIG.29

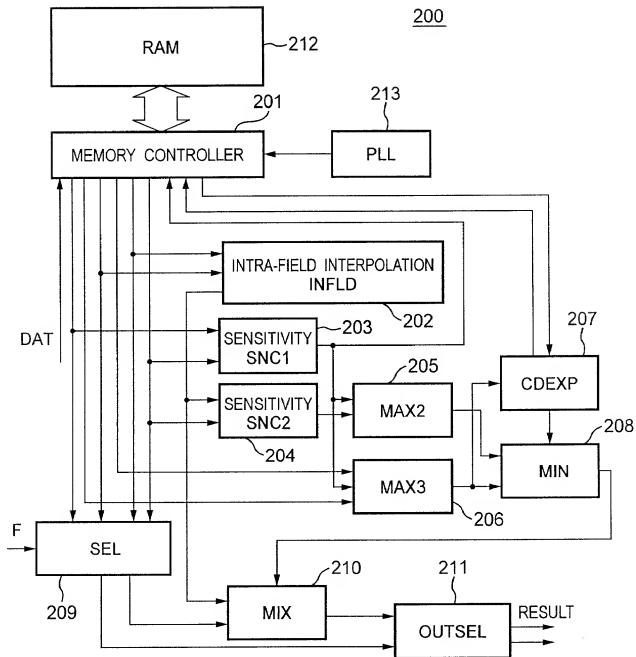


FIG. 30

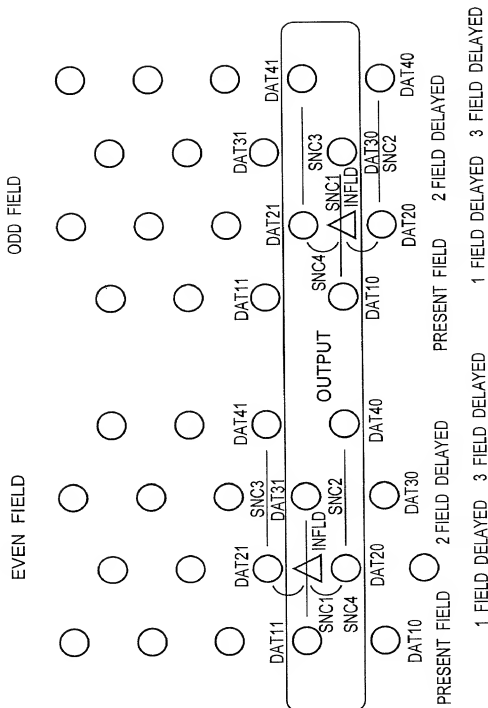
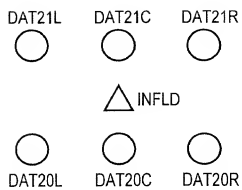


FIG.31



$$\begin{array}{c} \bigcirc \\ \bigcirc \\ \bigcirc \\ \bigcirc \\ \bigcirc \\ \bigcirc \\ \vdots \\ \bigcirc \\ \bigcirc \end{array} \quad \begin{array}{c} \bullet \\ \bullet \\ \bullet \\ \bullet \\ \bullet \\ \bullet \\ \vdots \\ \bullet \\ \bullet \end{array}$$

1 FRAME

FIG.33



FIG.34

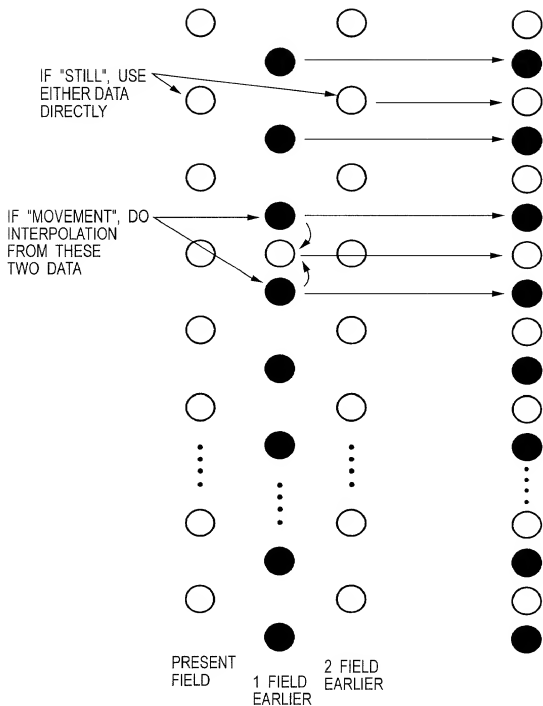


FIG.35

